

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
BUR920030056US1

In Re Application Of: **Davis S. Collins et al.**

NOV 14 2003

TRADEMARK OFFICE

Serial No.
10/605,960

Filing Date
Nov. 10, 2003

Examiner
Unassigned

Group Art Unit
Unassigned

Title: **ESD DESIGN, VERIFICATION AND CHECKING SYSTEM AND METHOD OF USE**

Address to:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

the statement specified in 37 CFR 1.97(e);

OR

the fee set forth in 37 CFR 1.17(p).

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ESD DESIGN, VERIFICATION AND CHECKING SYSTEM AND METHOD OF USE

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

A check in the amount of _____ is attached.

The Director is hereby authorized to charge and credit Deposit Account No. 09-0456 as described below.

Charge the amount of _____

Credit any overpayment.

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Certificate of Transmission by Facsimile*

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I certify that this document and fee is being deposited on 11/11/2003 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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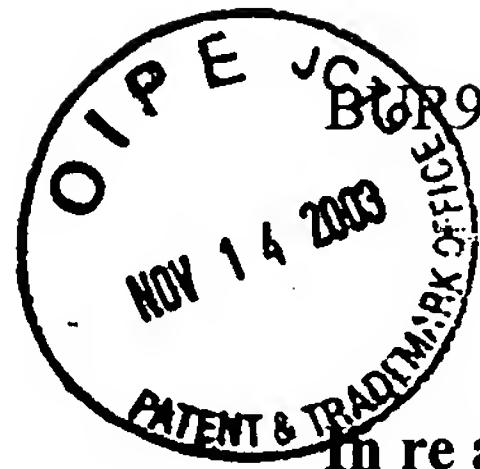
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Richard M. Kotulak
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Telephone: 802-769-4457

Dated: 11/11/2003

CC:



920030056US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

David S. Collins, et al.

Group Art Unit: Unknown

10/605, 960
Serial No.: Not assigned yet

Examiner: unknown

Filed: Concurrently herewith

For: ESD DESIGN, VERIFICATION AND CHECKING SYSTEM AND METHOD OF USE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.56**

Sir:

Under provisions of 37 C.F.R. 1.97 through 1.99 and pursuant to applicant's duty of disclosure under 37 C.F.R. 1.56, applicants respectfully bring the document listed on the attached Form PTO-1449 to the attention of the Examiner in charge of the above-identified application. A copy of the document cited is enclosed for the convenience of the Examiner.

This citation does not constitute an admission that the cited reference is relevant or material to the claims nor should it be construed as a representation that no other art than that identified exists. It is merely cited as constituting related art of which the applicant is aware.

It is respectfully requested that this document be considered by the Examiner and formally made of record in this application.

Respectfully submitted,

Andrew M. Calderon
Reg. No. 38,093

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.